

Avatar Deployment Diagram

```
graph TD
    subgraph CPU0 [CPU0]
        B[AVATAR Design::B]
        B0[AVATAR Design::B0]
        B1[AVATAR Design::B1]
    end
    subgraph CPU1 [CPU1]
        B2[AVATAR Design::B2]
    end
    Vgmn0[<<VGMN>> Vgmn0]
    Memory0[<<RAM>> Memory0]
    CPU0 --- Vgmn0
    CPU1 --- Vgmn0
    Vgmn0 --- Memory0
    Vgmn0 -- "AVATAR Design::B/out sendReq(int value) #--# B2/in receiveReq(int value)" --- Memory0
```

Avatar Design (AVATAR Design)

- Avatar Design (AVATAR Design)
- Avatar Deployment (Avatar Deployment)
- Validation
- Search result

Session2-7-5
Session2bis