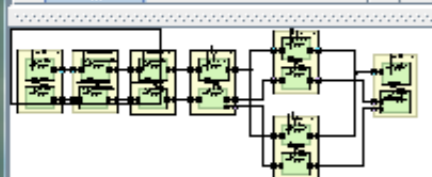


File Edit V&amp;V Code Generation View Tool Help



TTool: /home/andrea/TURTLE/papers/papersAndreaENRICI/Zigbee/models/ZigBeeDEMO.xml

- DIPLODOCUS\_Methodology
- ZigBee\_TX(DIPLODOCUS\_Methodology)
- Zigbee\_TX2(DIPLODOCUS\_Methodology)
- DIPLODOCUS\_Communication
- DIPLODOCUS\_Communication
- DIPLODOCUS\_Communication
- Embb (DIPLODOCUS\_Methodology)
- MappingTX\_0 (DIPLODOCUS\_Methodology)
- MappingTX\_1 (DIPLODOCUS\_Methodology)
- MappingTX\_2 (DIPLODOCUS\_Methodology)

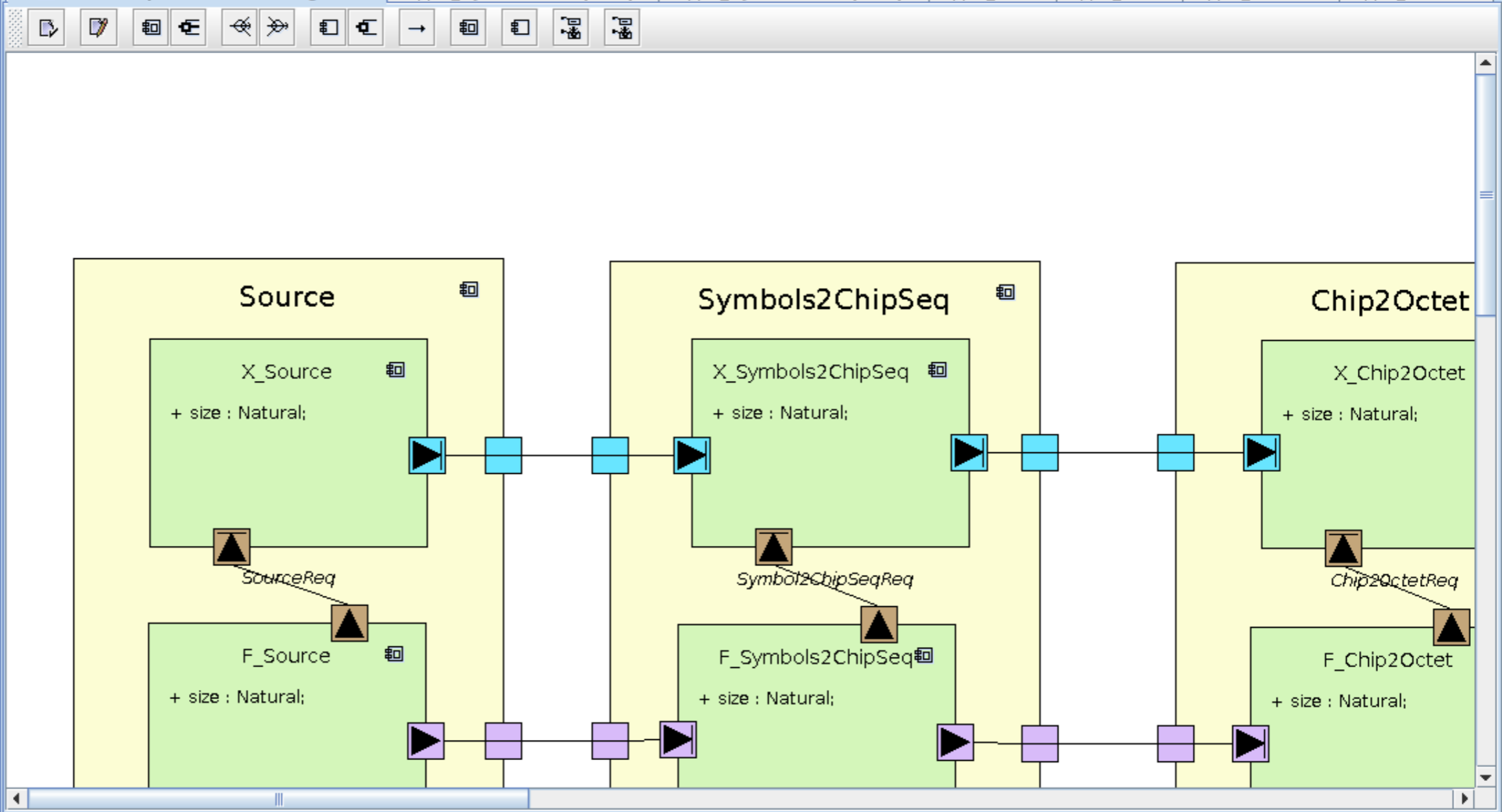


MappingTX\_0 MappingTX\_1 MappingTX\_2 MappingTX\_3 MappingTX2\_0 MappingTX2\_1 MappingTX2\_2

DIPLODOCUS\_Methodology ZigBee\_TX Zigbee\_TX2 CP\_Memory\_Copy DMA\_transfer Double\_DMA\_transfer Embb

X\_Chip2Octet F\_Chip2Octet X\_CWL F\_CWL X\_CWP\_I F\_CWP\_I X\_CWP\_Q F\_CWP\_Q

TML Component Task Diagram X\_Symbols2ChipSeq F\_Symbols2ChipSeq X\_Sink F\_Sink X\_Source F\_Source



Show / hide DIPLODOCUS IDs