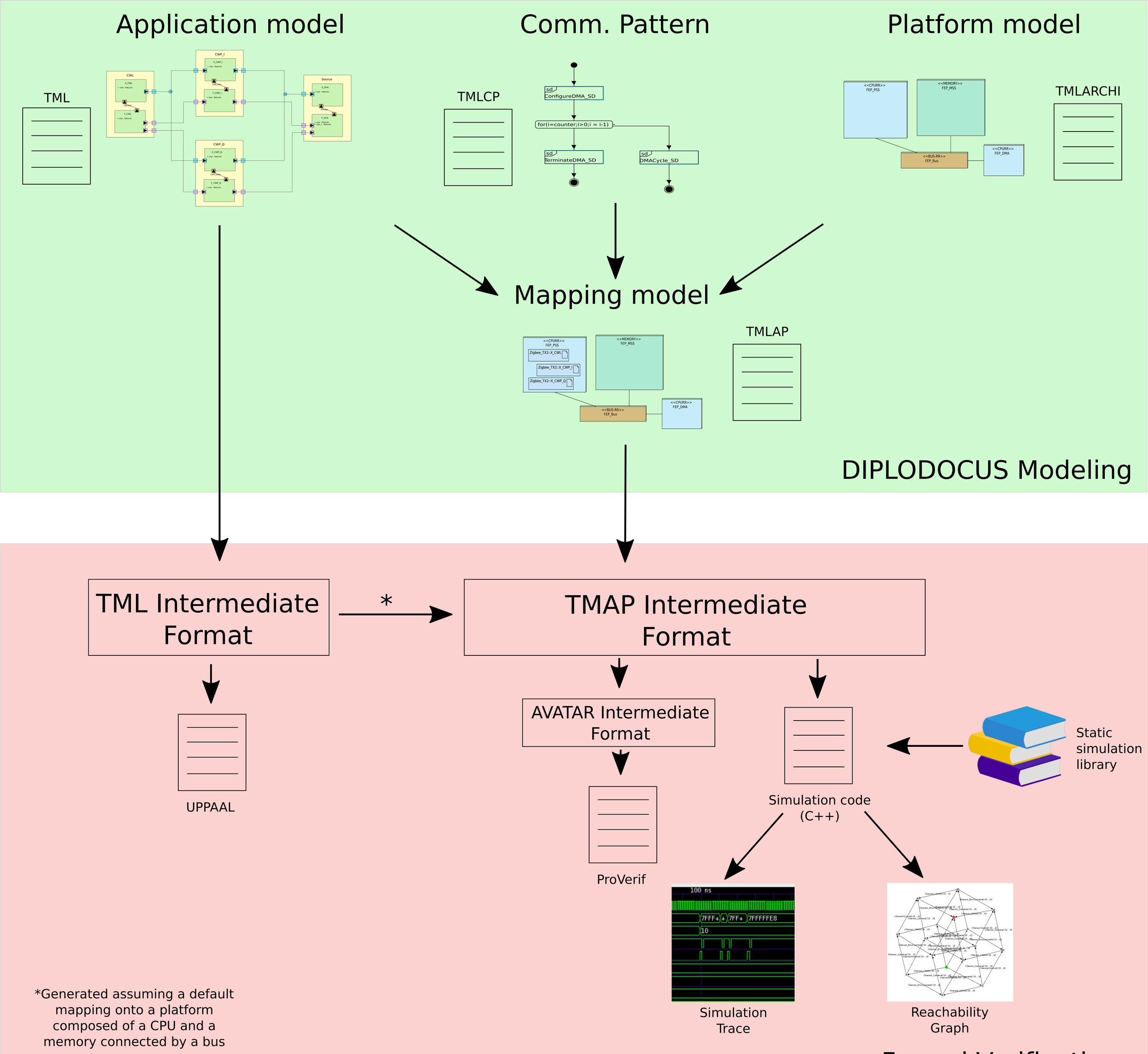
Application model
Comm. Pattern
Platform model
TML
TMLCP
TMLARCHI
ConfigureDMA_SD
for(i=counter;i>0;i = i-1)
TerminateDMA_SD
DMACycle_SD
Mapping model
TMLAP
DIPLODOCUS Modeling
TML Intermediate Format
*
TMAP Intermediate Format
AVATAR Intermediate Format
UPPAAL
ProVerif
Simulation code (C++)
Static simulation library
Simulation Trace
Reachability Graph
*Generated assuming a default mapping onto a platform composed of a CPU and a memory connected by a bus
Formal Verification